



	LCA127L	Units
Load Voltage	250	V
Load Current	170	mA
Max R _{ON}	15	Ω

Features

- Small 6 Pin DIP Package
- Low Drive Power Requirements (TTL/CMOS Compatible)
- No Moving Parts
- High Reliability
- Arc-Free With No Snubbing Circuits
- 3750V_{RMS} Input/Output Isolation
- FCC Compatible
- VDE Compatible
- No EMI/RFI Generation
- Machine Insertable, Wave Solderable
- Current Limiting, Surface Mount and Tape & Reel Versions Available

Applications

- Telecommunications
 - Telecom Switching
 - Tip/Ring Circuits
 - Modem Switching (Laptop, Notebook, Pocket Size)
- Hookswitch
- Dial Pulsing
- Ground Start
- Ringer Injection
- Instrumentation
 - Multiplexers
 - Data Acquisition
 - Electronic Switching
 - I/O Subsystems
 - Meters (Watt-Hour, Water, Gas)
- Medical Equipment-Patient/Equipment Isolation
- Security
- Aerospace
- Industrial Controls

Description

LCA127L is a 250V, 170mA, 15Ω 1-Form-A relay. It features low on-resistance.

Approvals

- UL Recognized: File Number E76270
- CSA Certified: File Number LR 43639-10
- BSI Certified:
 - BS EN 60950:1992 (BS7002:1992) Certificate #:7344
 - BS EN 41003:1993 Certificate #:7344

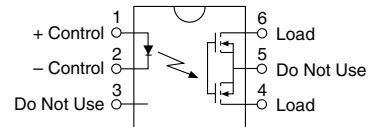
Ordering Information

Part #	Description
LCA127L	6 Pin DIP (50/Tube)
LCA127LS	6 Pin Surface Mount (50/Tube)
LCA127LSTR	6 Pin Surface Mount (1000/Reel)

Pin Configuration

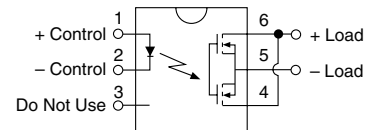
LCA127L Pinout

AC/DC Configuration

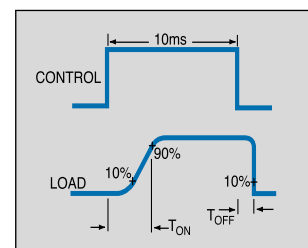


LCA127L Pinout

DC Only Configuration



Switching Characteristics of Normally Open (Form A) Devices





LCA127L

Absolute Maximum Ratings (@ 25° C)

Parameter	Min	Typ	Max	Units
Input Power Dissipation	-	-	150 ¹	mW
Input Control Current	-	-	50	mA
Peak (10ms)	-	-	1	A
Reverse Input Voltage	-	-	5	V
Total Power Dissipation	-	-	800 ²	mW
Capacitance				
Input to Output	-	3	-	pF
Isolation Voltage				
Input to Output	3750	-	-	V _{RMS}
Operational Temperature	-40	-	+85	°C
Storage Temperature	-40	-	+125	°C
Soldering Temperature	-	-	-	-
DIP Package	-	-	+260	°C
Flatpack/Surface Mount Pkg (10 Seconds Max.)	-	-	+220	°C

¹ Derate Linearly 1.33 mw/°C

² Derate Linearly 6.67 mw/°C

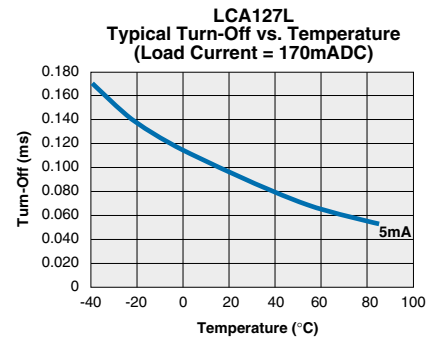
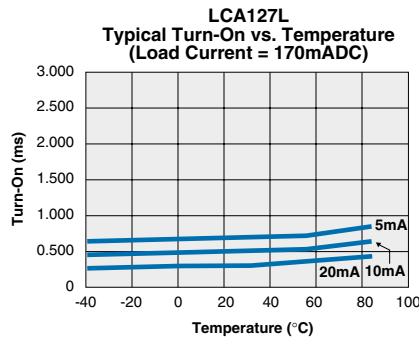
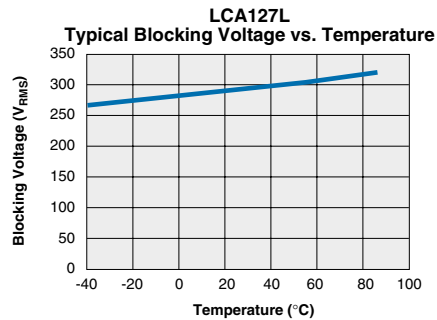
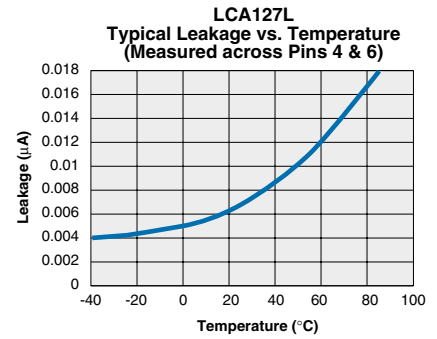
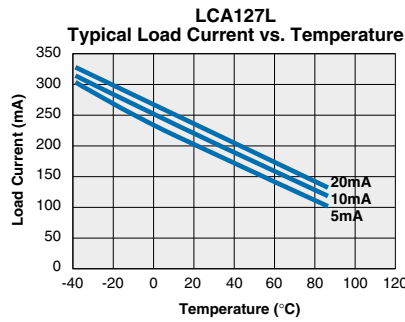
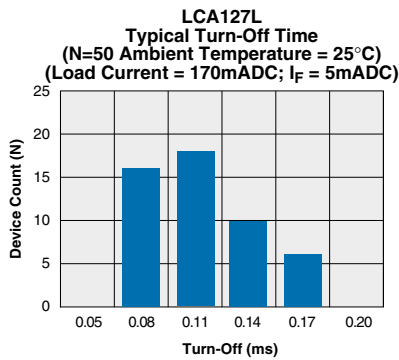
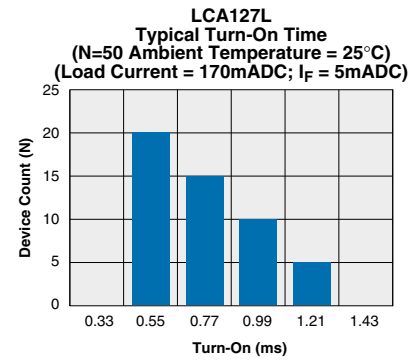
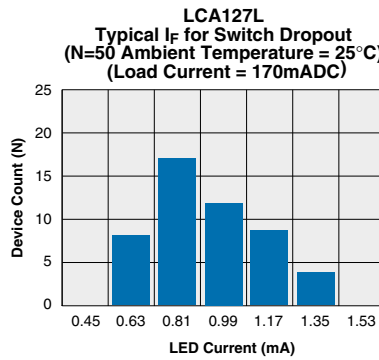
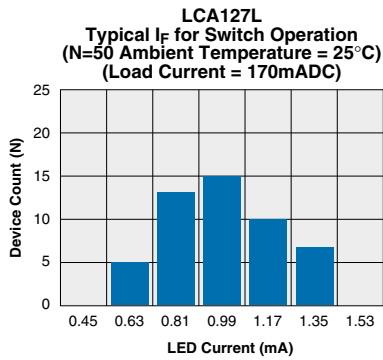
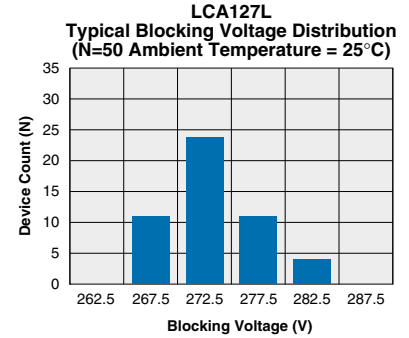
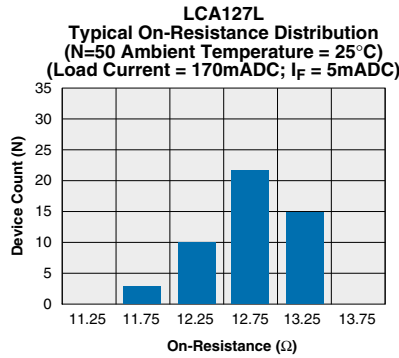
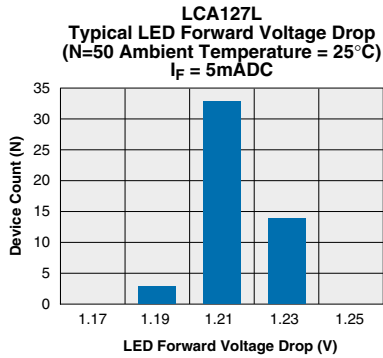
Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this data sheet is not implied. Exposure of the device to the absolute maximum ratings for an extended period may degrade the device and effect its reliability.

Electrical Characteristics

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics @ 25°C						
Load Voltage (Peak)	-	V _L	-	-	250	V
Load Current* (Continuous)						
AC/DC Configuration	-	I _L	-	-	170	mA
DC Configuration	-	I _L	-	-	250	mA
Peak Load Current	10ms	I _{LPK}	-	-	-	mA
On-Resistance						
AC/DC Configuration	I _L =200mA	R _{ON}	-	13	15	Ω
DC Configuration	I _L =200mA	R _{ON}	-	5	6	Ω
Off-State Leakage Current	V _L =250V	I _{LEAK}	-	-	1	μA
Switching Speeds						
Turn-On	I _F =5mA, V _L =10V	T _{ON}	-	-	5	ms
Turn-Off	I _F =5mA, V _L =10V	T _{OFF}	-	-	5	ms
Output Capacitance	50V; f=1MHz	C _{OUT}	-	50	-	pF
Load Current Limiting	-	I _{CL}	190	235	280	mA
Input Characteristics @ 25°C						
Input Control Current	I _L = 200mA	I _F	5	-	50	mA
Input Dropout Current	-	I _F	0.4	0.7	-	mA
Input Voltage Drop	I _F = 5mA	V _F	0.9	1.2	1.4	V
Reverse Input Voltage	-	V _R	-	-	5	V
Reverse Input Current	V _R =5V	I _R	-	-	10	μA
Input to Output Capacitance	-	C _{I/O}	-	3	-	pF
Input to Output Isolation	-	V _{I/O}	3750	-	-	V _{RMS}

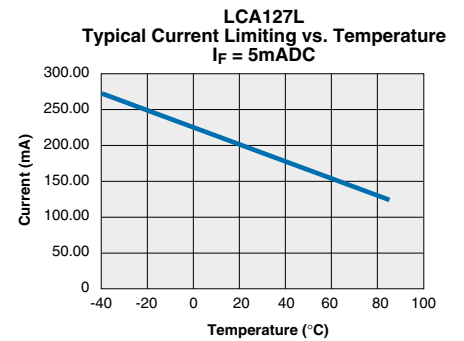
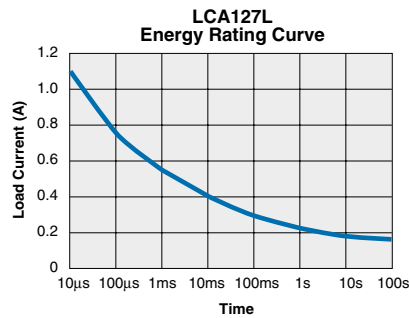
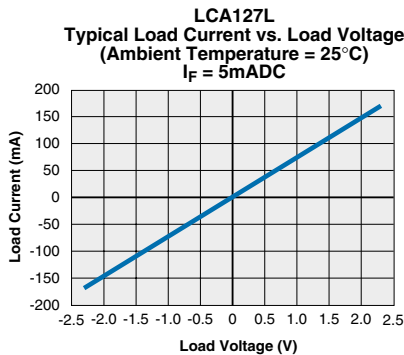
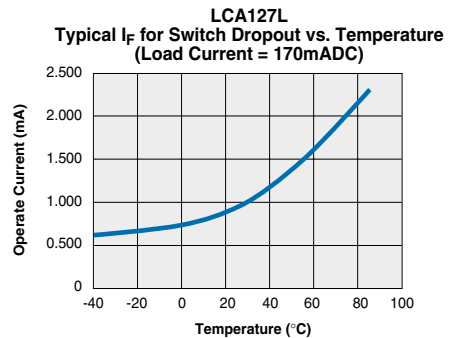
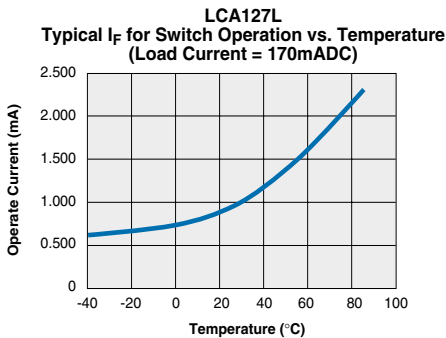
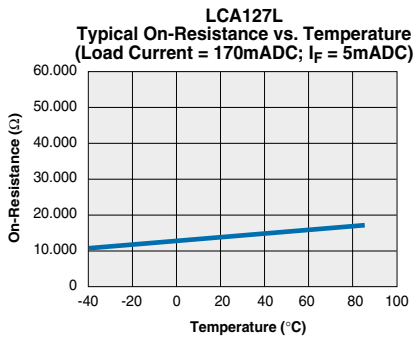
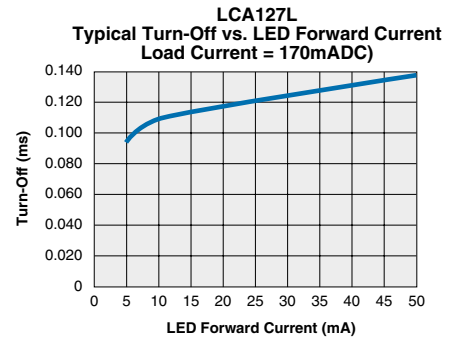
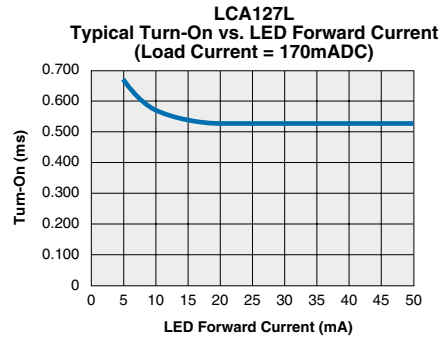
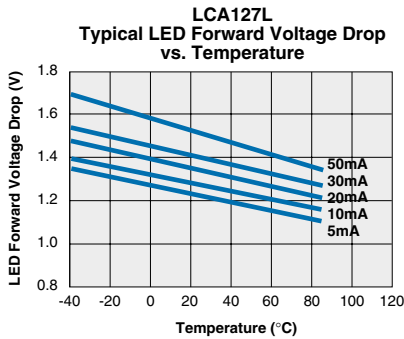
*NOTE: If both poles operate simultaneously load current must be derated so as not to exceed the package power dissipation value.

PERFORMANCE DATA*



*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

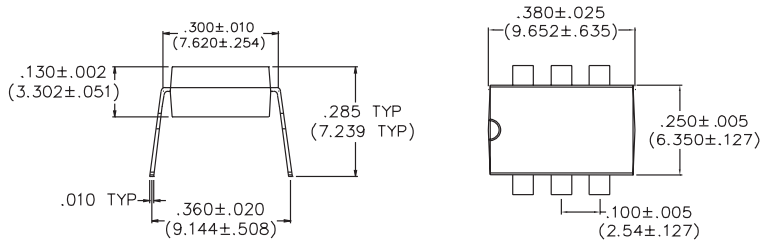
PERFORMANCE DATA*



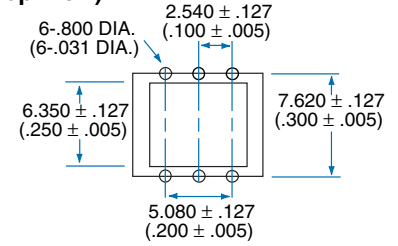
*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

MECHANICAL DIMENSIONS

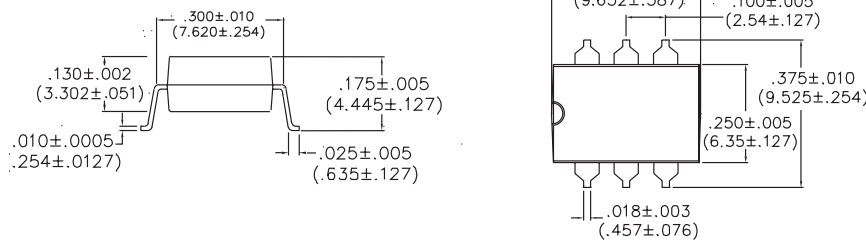
6Pin DIP Through Hole (Standard)



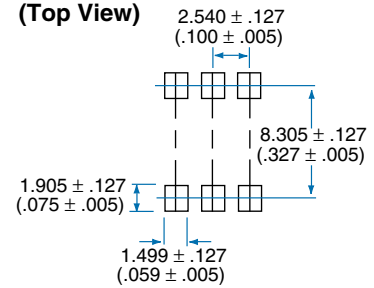
PC Board Pattern (Top View)



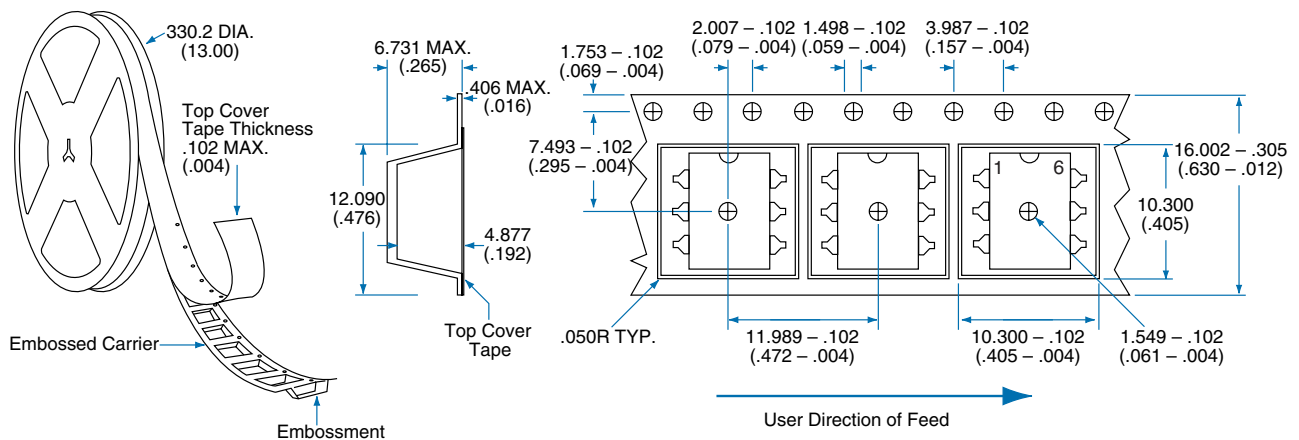
6Pin DIP Surface Mount ("S" Suffix)



PC Board Pattern (Top View)



Tape and Reel Packaging for 6 Pin Power DIP Surface Mount Package



Dimensions
mm
(inches)



CLARE

For additional information please visit our website at: www.clare.com

Clare, Inc. makes no representations or warranties with respect to the accuracy or completeness of the contents of this publication and reserves the right to make changes to specifications and product descriptions at any time without notice. Neither circuit patent licenses nor indemnity are expressed or implied. Except as set forth in Clare's Standard Terms and Conditions of Sale, Clare, Inc. assumes no liability whatsoever, and disclaims any express or implied warranty, relating to its products including, but not limited to, the implied warranty of merchantability, fitness for a particular purpose, or infringement of any intellectual property right.

The products described in this document are not designed, intended, authorized or warranted for use as components in systems intended for surgical implant into the body, or in other applications intended to support or sustain life, or where malfunction of Clare's product may result in direct physical harm, injury, or death to a person or severe property or environmental damage. Clare, Inc. reserves the right to discontinue or make changes to its products at any time without notice.

Specification: DS-LCA217L-R2.0
©Copyright 2002, Clare, Inc.
OptoMOS® is a registered trademark of Clare, Inc.
All rights reserved. Printed in USA.
5/31/02